

PATENT NUMBER

U.S. PATENT OFFICE	O.I.P.E.	PATENT DATE
A-2 SCANNED 10-10-10	10-10-10 10-10-10 10-10-10	10-10-10 10-10-10 10-10-10

APPLICATION NO. 09/942102	CONT/PRIOR	CLASS 700	SUBCLASS	ART UNIT 2421	EXAMINER Hartman
APPLICANTS William Wheeler Matthew Adiletta		716	14	2825	
TITLE Generating a logic design					

ISSUING CLASSIFICATION											
ORIGINAL				CROSS REFERENCE(S)							
CLASS		SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION											

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Primary Examiner)		ISSUE FEE	
			Amount Due	Date Paid
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	

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